

U.S. PATENT DOCUMENTS[illegible]

		Pub. No.	Pub. Date	Patentee	CLASS	SUB-CLASS	FILING DATE

[illegible]

F.A.		<p>Mohammad M. Mansour et al., "Design Methodology for High-Speed Iterative Decoder Architectures", <u>Proceeding of IEEE Conference on Acoustics, Speech, and Signal Processing</u>, Vol. 3, pages III-3085 - III-3088 (May 2002).</p>
1		<p>Alexander Worm et al., "VLSI Architectures for High-Speed MAP Decoders", <u>Proceeding of 14th International Conference on VLSI Design</u>, pages 446-453 (January 2001).</p>

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw Line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)				ATTY. DOCKET NO. 45565		SERIAL NO. 10/634,746		
				APPLICANT Sun-Jin Park, et al.				
				FILING DATE August 6, 2003		GROUP Not Assigned		
U.S. Patent Documents								
		Patent No.	Date	Patentee	Class	Sub-class	Filing Date	
Published U.S. Patent Application								
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Foreign Patent Documents or Foreign Patent Applications								
		Document No.	Pub. Date	Country or Patent Office	Class	Sub-class	English - Abstract	
							Yes No	
<i>F.A.</i>		1261139	11-2002	EPO			X	
Other Documents (including Arthur, Title, Date, Pertinent Pages, etc.)								
<i>F.A.</i>		Gross, Warren J., Gaudet, Vincent C., Gulak, P. Glenn, "Difference Metric Soft-Output Detection: Architecture and Implementation" pp. 1-8, figure 7, September 2001.						
		Sklavos, N., Kitsos, P., Zervas, N., & Koufopavlou, O.; "A New Low Power and high Speed Bidirectional Shift Register Architecture", figures 1 and 2, July 2001.						
		Masera, G., et al., "VLSI Architectures for Turbo Codes", IEEE Transactions on Very Large Scale Integration (VLSI) Systems, IEEE Inc., New York, vol. 7, no. 3, pp. 369-379, September 1999.						
		"Bidirectional Shift Registers", February 1999.						
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